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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74LS42

BCD-to-Decimal Decoder

REJ03D0409-0300 Rev.3.00 Jul.22.2005

This monolithic decimal decoder consists of eight inverters and ten four-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by NAND gates. Full decoding of valid input logic ensures that all outputs remain off for all invalid input conditions.

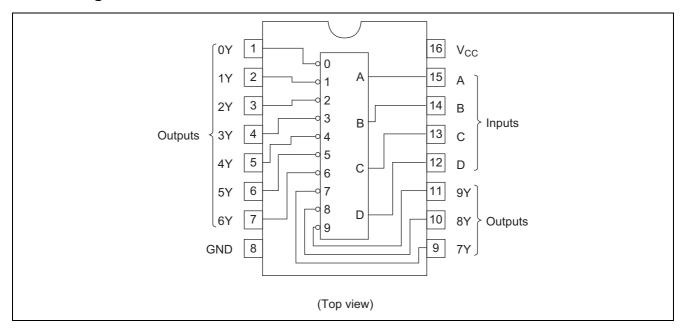
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS42P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74LS42FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74LS42RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement

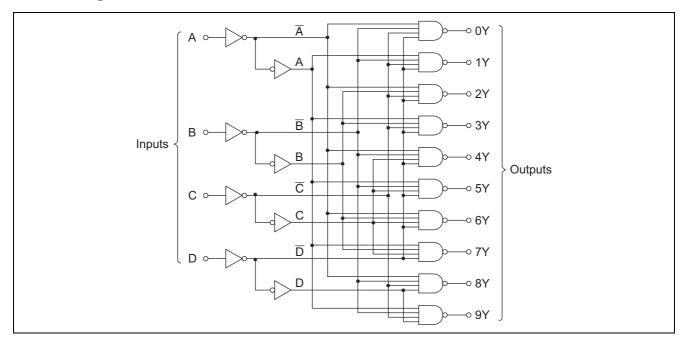


Function Table

No.		BCD	input			Decimal output								
NO.	D	С	В	Α	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
1	L	L	L	Н	Ι	L	Н	Н	Н	Н	Ι	Н	Н	Н
2	L	L	Η	L	Ι	Н	L	Н	Н	Н	Ι	Н	Н	Н
3	L	L	Η	Н	Ι	Н	Н	L	Н	Н	Ι	Н	Н	Н
4	L	Н	L	L	Ι	Н	Н	Н	L	Н	Ι	Н	Н	Н
5	L	Н	L	Н	Ι	Н	Н	Н	Н	L	Ι	Н	Н	Н
6	L	Н	Η	L	Ι	Н	Н	Н	Н	Н	L	Н	Н	Н
7	L	Н	Η	Н	Ι	Н	Н	Н	Н	Н	Ι	L	Н	Н
8	Ι	L	L	L	Ι	Н	Н	Н	Н	Н	Ι	Н	L	Н
9	Ι	L	L	Н	Ι	Н	Н	Н	Н	Н	Ι	Н	Н	L
	Ι	L	Η	L	Ι	Н	Н	Н	Н	Н	Ι	Н	Н	Н
	Ι	L	Η	Н	Ι	Н	Н	Н	Н	Н	Ι	Н	Н	Н
Invalid	Ι	Н	L	L	Ι	Н	Н	Н	Н	Н	Ι	Н	Н	Н
Invalid	Η	Н	L	Н	Η	Н	Н	Н	Н	Н	Η	Н	Н	Н
	Ι	Н	Η	L	Ι	Н	Н	Н	Н	Н	Η	Н	Н	Н
	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

H; high level, L; low level

Block Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V_{CC}	4.75	5.00	5.25	V
Output current	I _{ОН}	_	_	-400	μΑ
	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V _{IH}	2.0	_	_	V	
Input voltage	VIL	_	_	0.8	V	
	V _{OH}	2.7			V	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V},$
Output voltage	VOH	2.1			V	$I_{OH} = -400 \mu A$
Output voltage	V _{OL}	_		0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$
		_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$
Input current	I _{IH}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
	I _{IL}	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 0.4 \text{ V}$
	I ₁	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$
Short-circuit output	laa	-20		-100	mA	V _{CC} = 5.25 V
current	I _{OS}	-20	_	-100	IIIA	VCC = 3.23 V
Supply current	I _{CC} **	_	7	13	mA	V _{CC} = 5.25 V
Input clamp voltage	V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Switching Characteristics

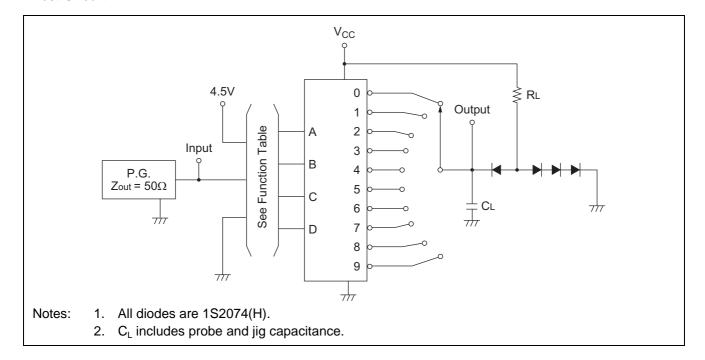
 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

Item		Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	2 Stage	t _{PLH}		15	25	ns	$-C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$
	3 Stage		_	20	30		
	2 Stage	4	_	15	25	ns	
	3 Stage	t _{PHL}	_	20	30		

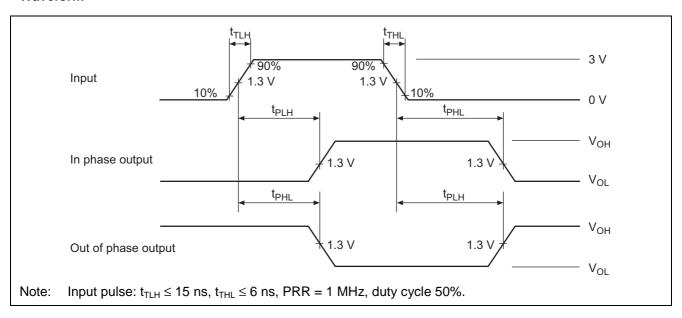
Notes: * $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$ ** V_{CC} is measured with all outputs and all inputs grounded.

Testing Method

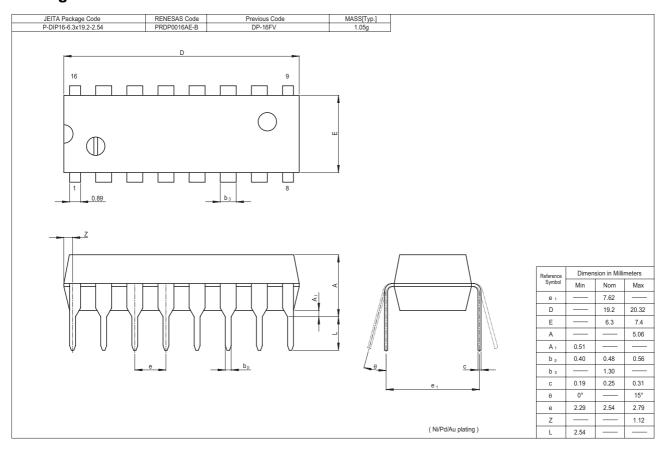
Test Circuit

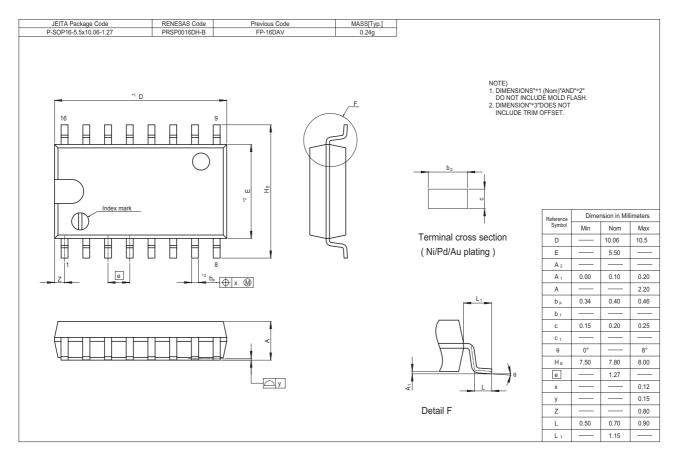


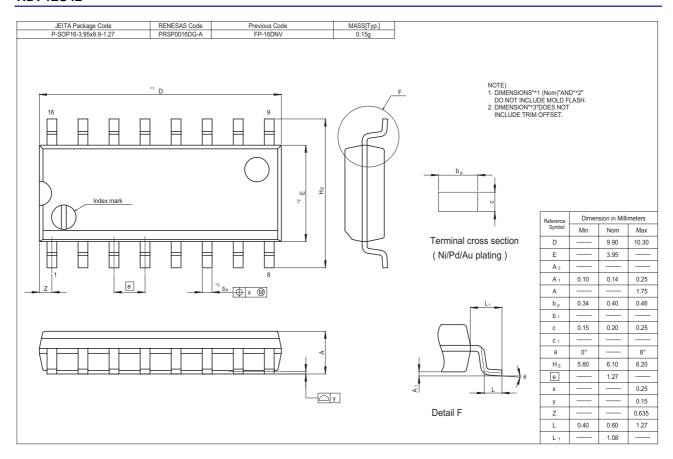
Waveform



Package Dimensions







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